

Electronic Patent Application Fee Transmittal

Application Number:	10807286			
Filing Date:	24-Mar-2004			
Title of Invention:	SEMICONDUCTOR INTEGRATED CIRCUIT TIMING ANALYSIS APPARATUS TIMING ANALYSIS METHOD AND TIMING ANALYSIS PROGRAM			
First Named Inventor/Applicant Name:	Tetsuo Kawano			
Filer:	Harry J. Staas/Renee Adams			
Attorney Docket Number:	1450.1040			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700